

Figure 1B

Figure 1A

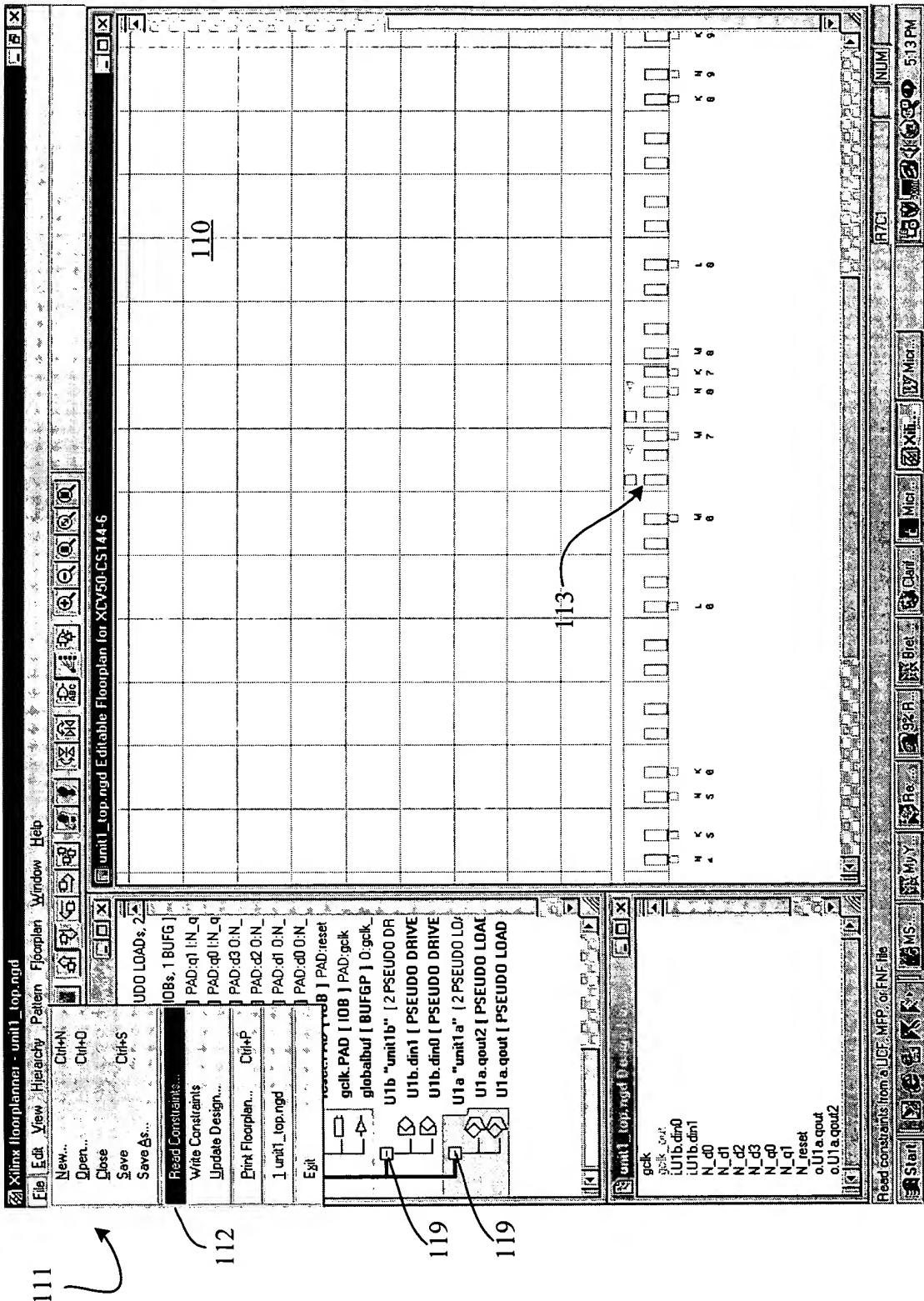


Figure 1C

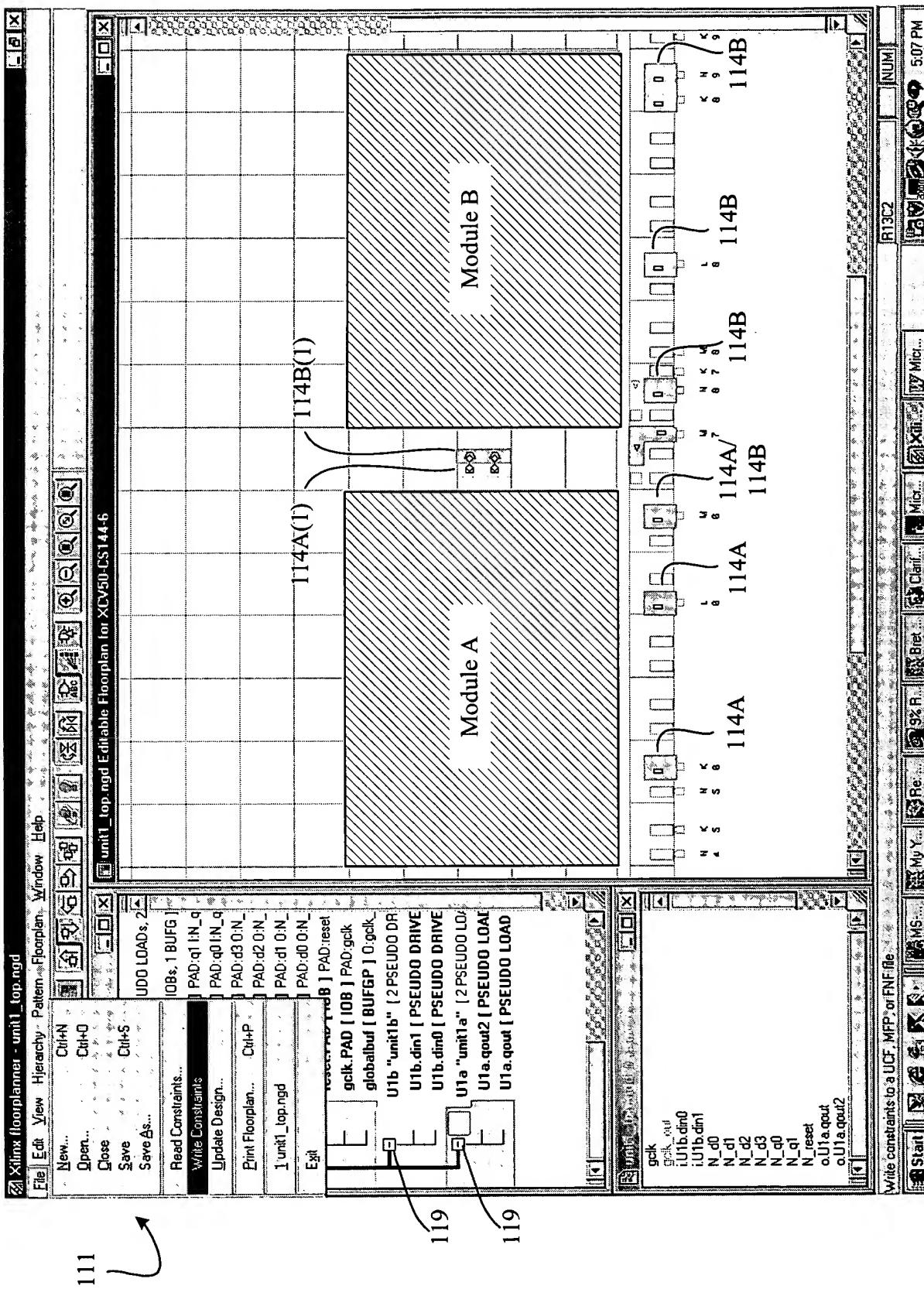


Figure 1D

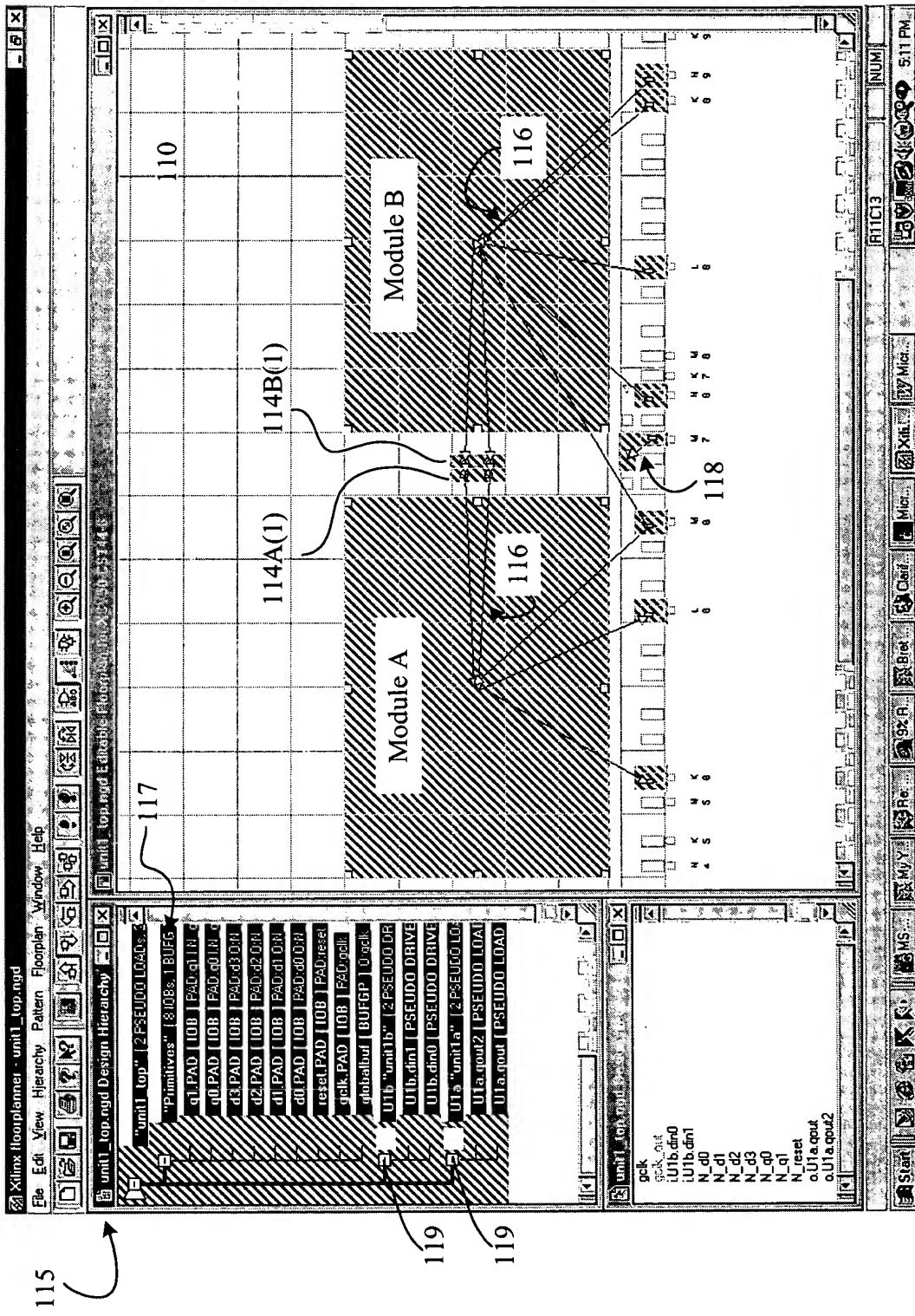


Figure 1E

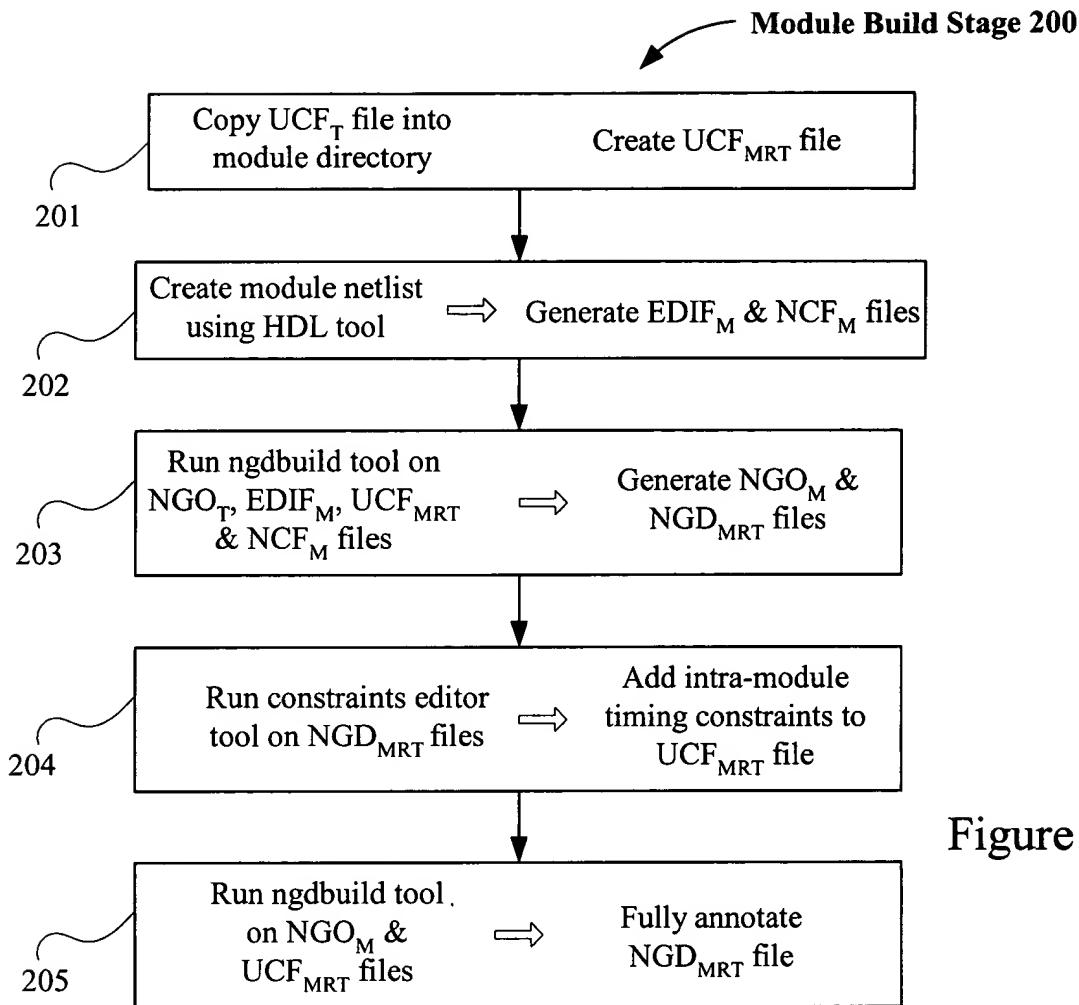


Figure 2A

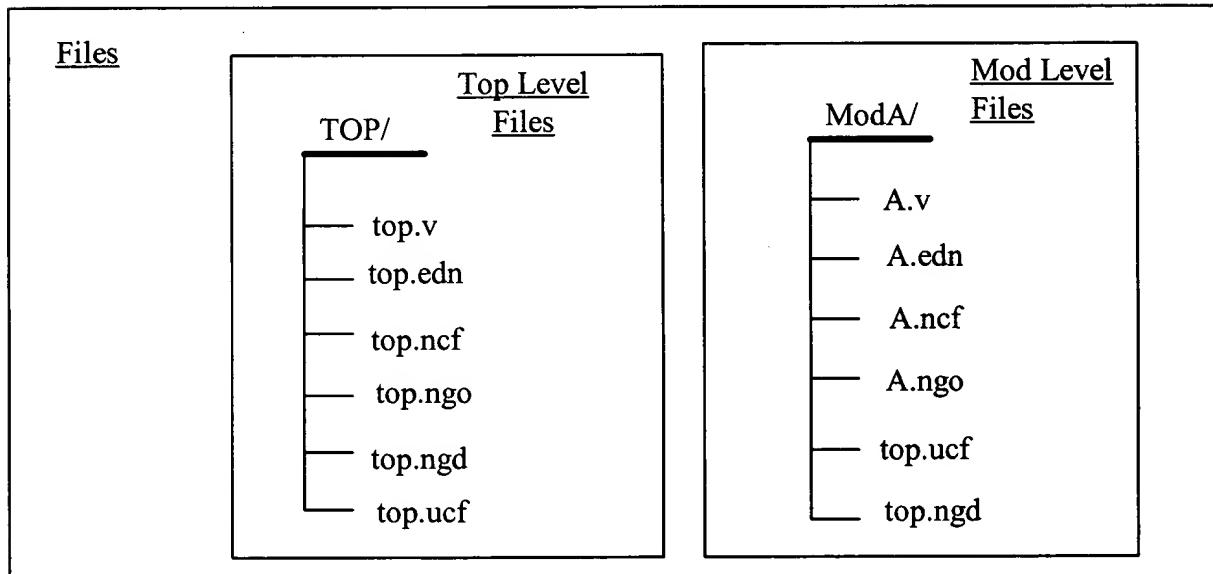


Figure 2B

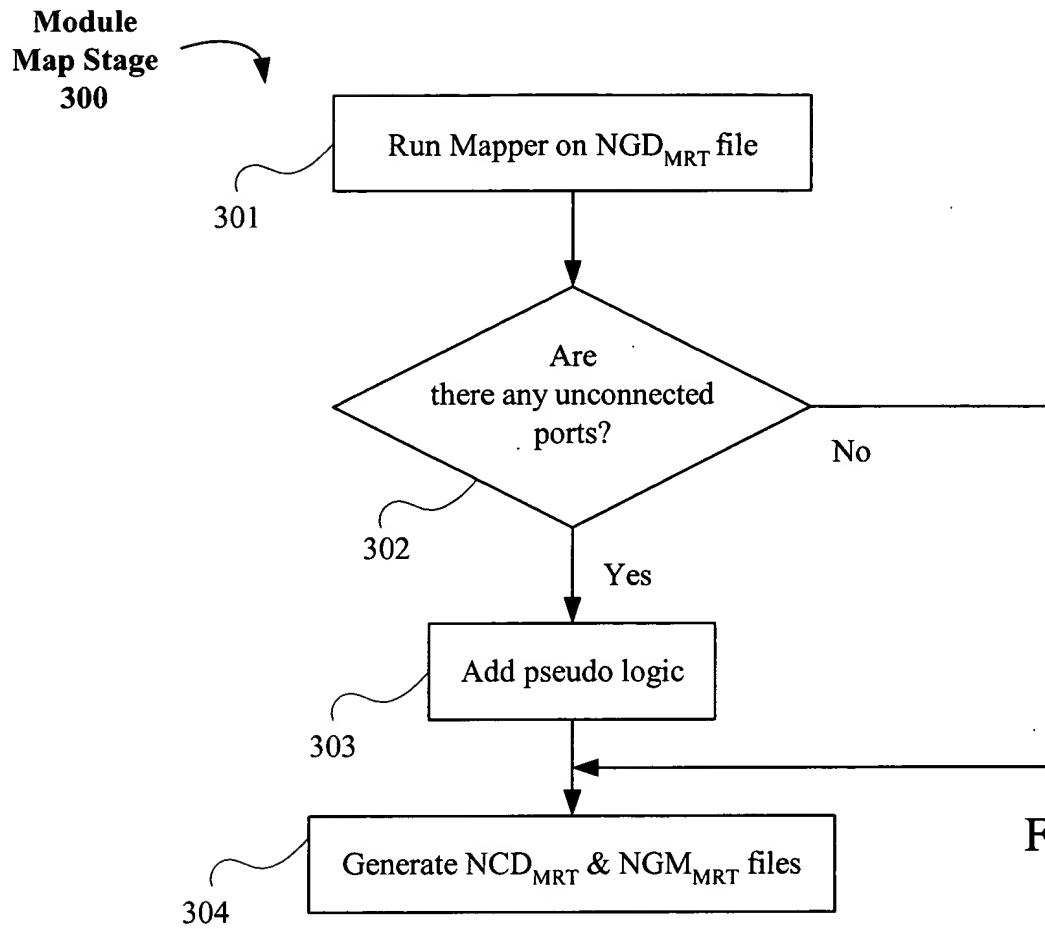


Figure 3A

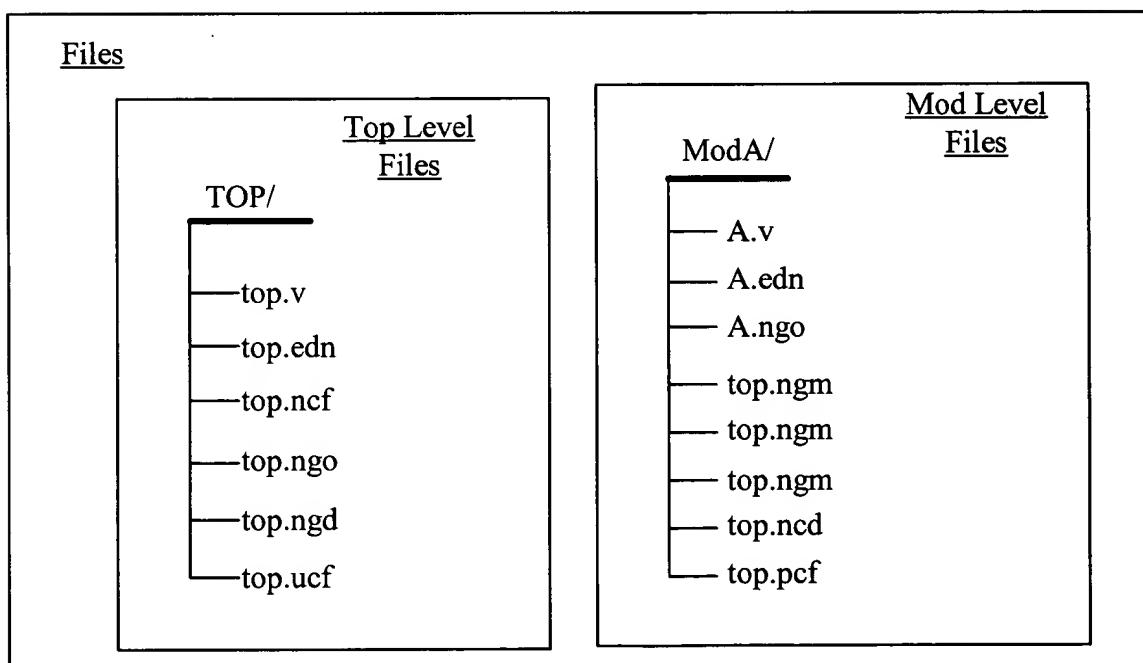


Figure 3B

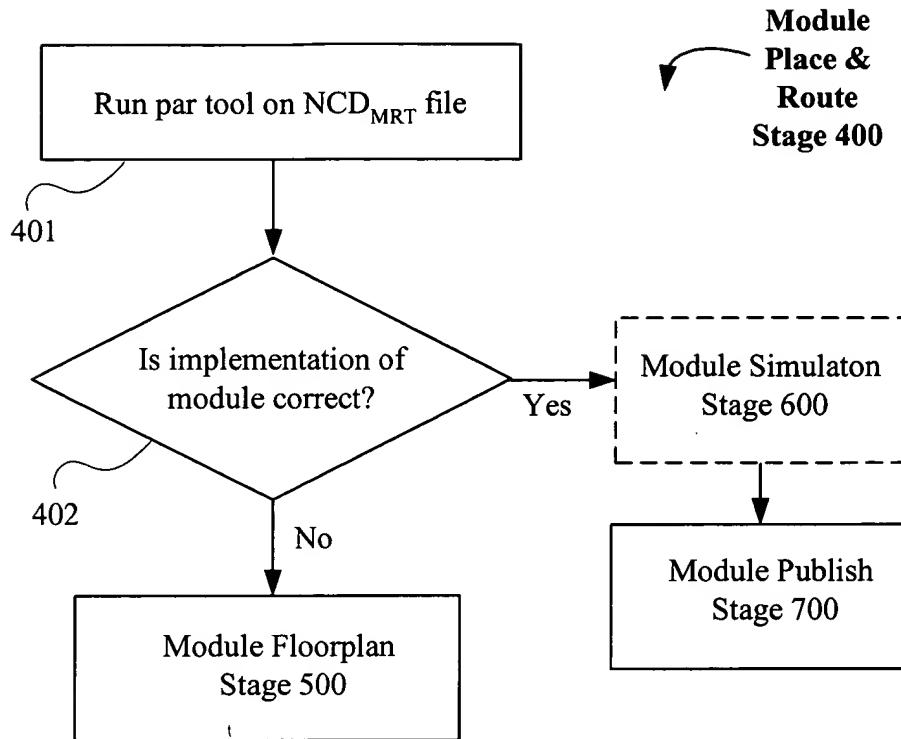


Figure 4

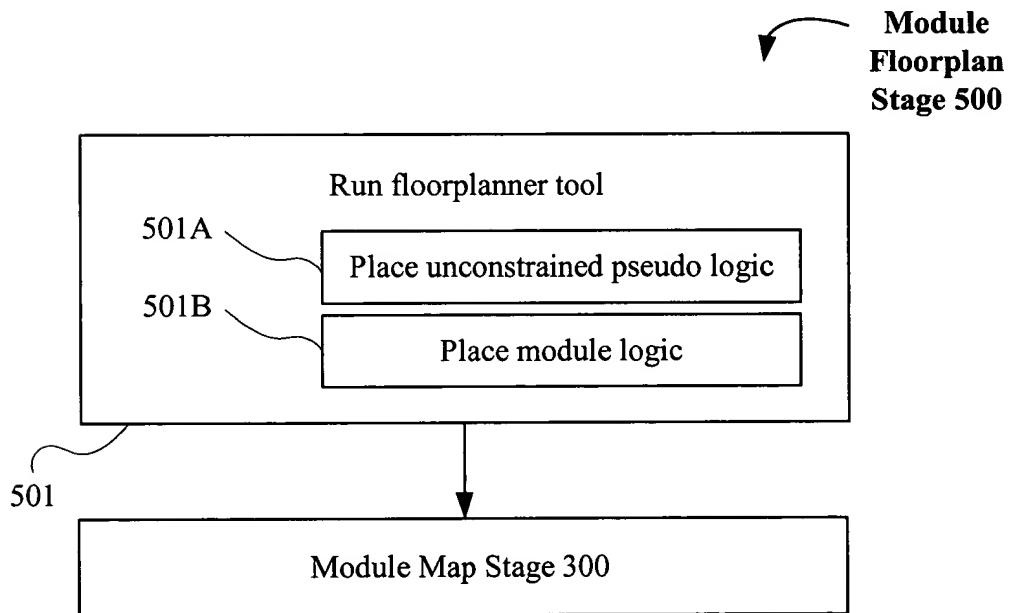


Figure 5

Module
Simulation
Stage 600

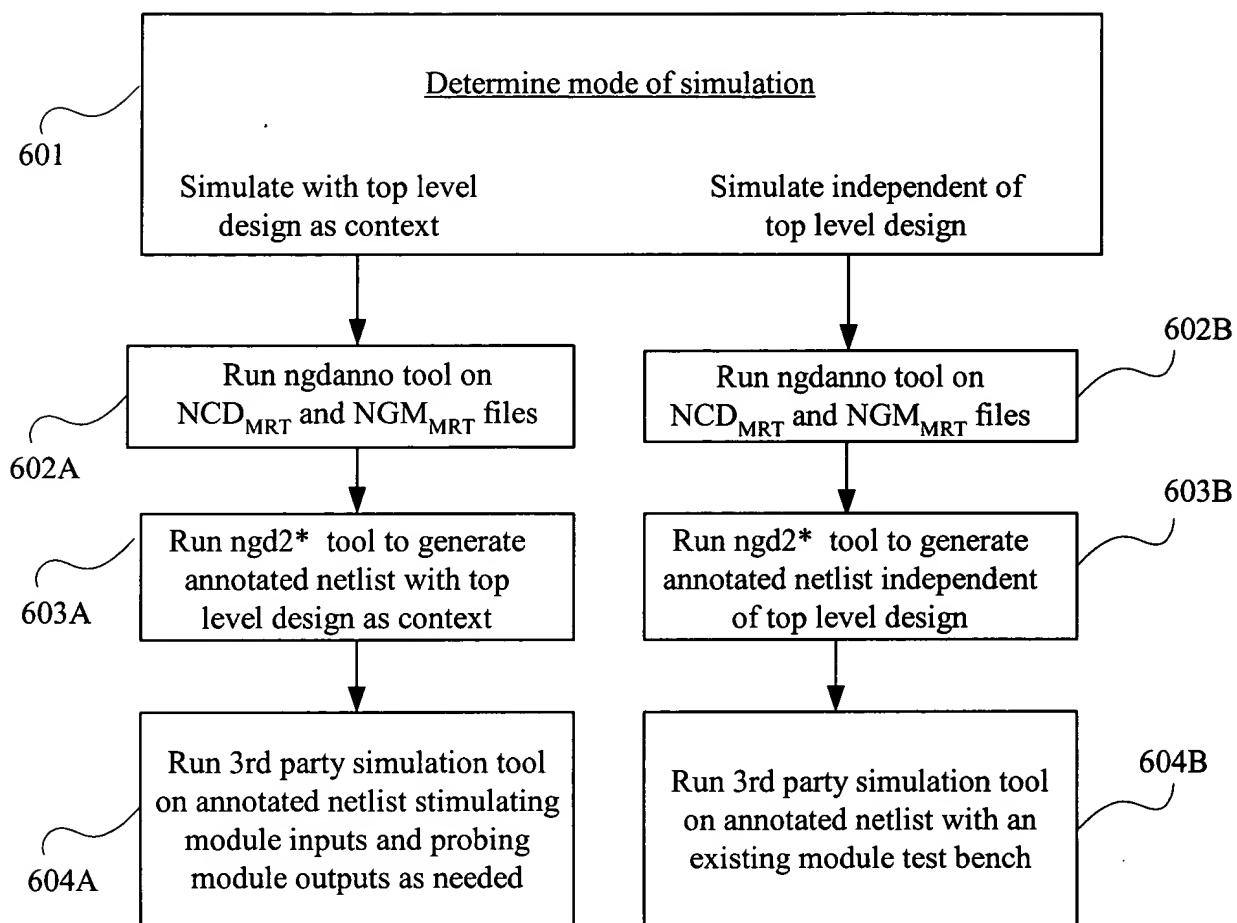


Figure 6

Module
Publishing
Stage 700

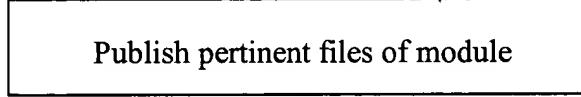


Figure 7

Final
Implementation
Stage 800

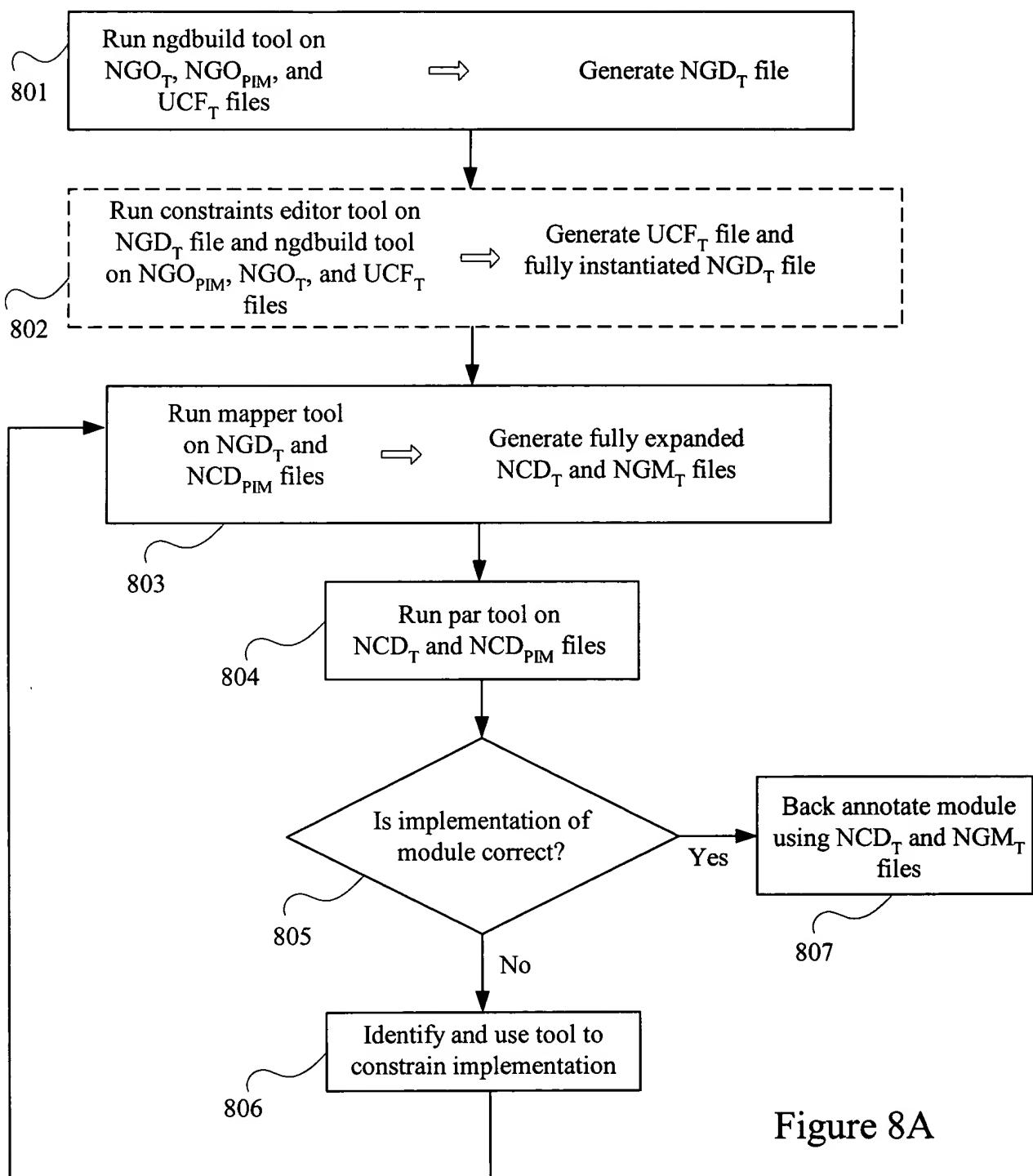


Figure 8A

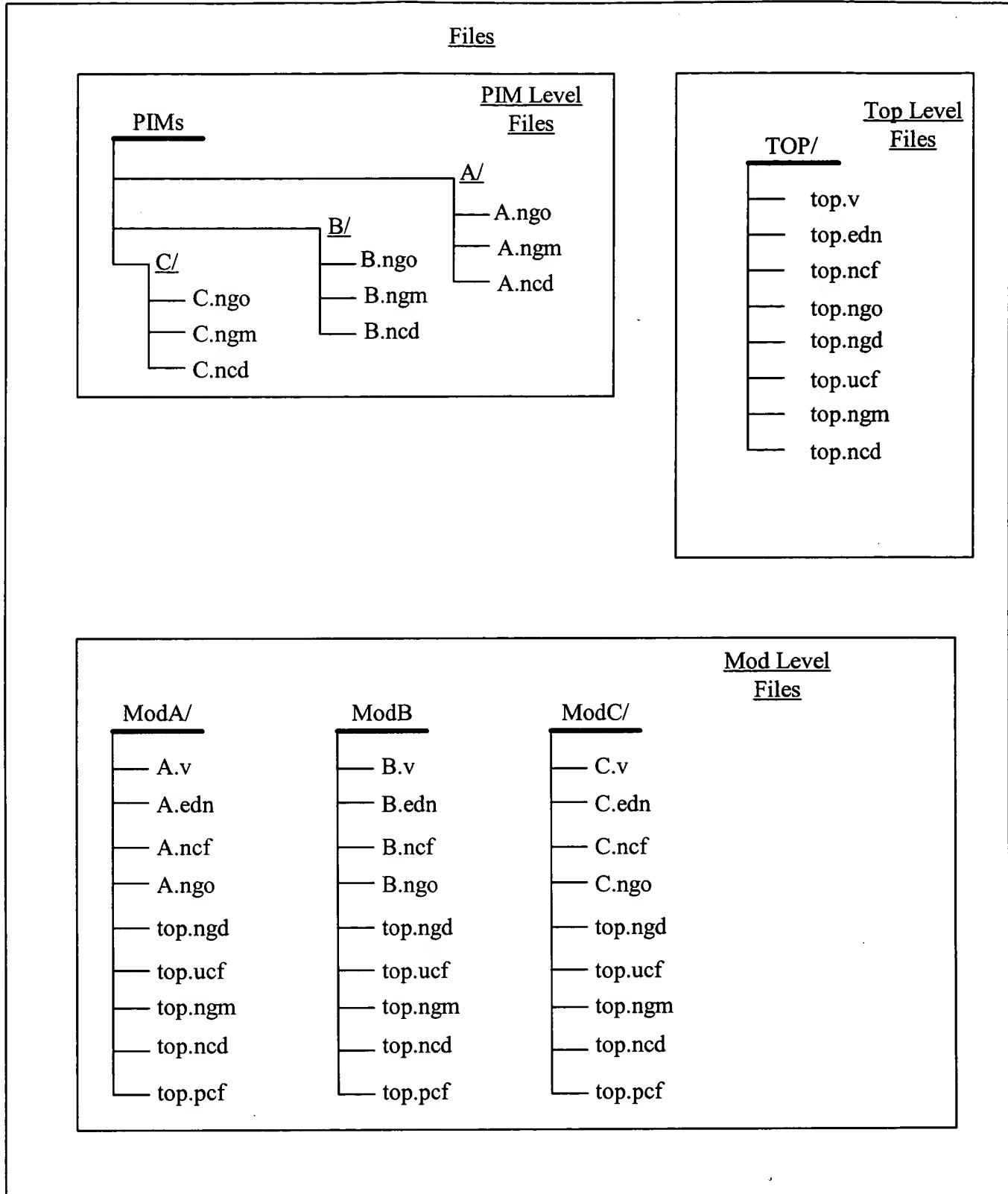


Figure 8B